



Examiner: Phallaka Kik

Group Art Unit: 2825

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kiran Ganesh et al.

Serial No.: Filed:

09/431477

Title

November 1, 1999 Docket No: 884.141US1 2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR

VLSI DESIGN

PETITION FOR A FIVE-MONTH EXTENSION OF TIME

Mail Stop RCE Commissioner for Patents P.O. Box 1450

In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that a five-month extension of time be granted in which to file the Appeal Brief; Notice of Appeal mailed December 26, 2003, said period of response being extended from February 26, 2003 to July 26, 2003.

Please charge Deposit Account No. 19-0743 in the amount of \$1970.00 to cover the required extension fee. Please charge any additional fees or credit overpayment to deposit Account No. 19-0743.

Respectfully Submitted

KIRAN GANESH ET AL.

By their Representatives,

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CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelop addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 2 / day of July 2003

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